



IBM DOCKET: FIS920030379US1

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

INVENTOR:	Lars W. Liebmann et al.)	EXAMINER:	
SERIAL NO.:	10/708,055)	ART UNIT:	1756
FILING DATE:	February 5, 2004)	DATE:	June 16, 2004
FOR:	Method of Conflict Avoidance in Fabrication of Gate-Shrink Alternating Phase Shifting Masks)		

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Mail Stop _____
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Dear Sir:

In accordance with 37 CFR 1.56, 1.97 and 1.98, the following items are made of record to assist the Patent & Trademark Office in its examination of this application and is, in the opinion of the attorney designated below for applicant(s), information relevant to the closest prior art of which that person is aware. The filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made or that no other art than that identified exists.

OTHER PUBLICATIONS

"Alternating Phase Shifted Mask for Logic Gate Levels, Design and Mask Manufacturing", Lars Liebmann, Ioana Graur, William Leipold, James Oberschmidt, David O'Grady, Denis Regaill; IBM Microelectronics, East Fishkill, New York and Burlington, Vermont. SPIE Conference on Optical Microlithography XII, March 1999, Vol. 3679, Pages 27-37.

A copy of each of the foregoing items and Form PTO 1449 are enclosed herewith.

Respectfully submitted,

By:



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CERTIFICATE OF MAILING UNDER 37 CFR 1.10

I hereby certify that, on the date shown below, this correspondence is being deposited with the United States Postal Service in an envelope addressed to Mail Stop _____ Commissioner of Patents P.O. Box 1450, Alexandria, VA 22313-1450, as U.S. First Class Mail postage pre-paid.

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mailing paper Barbara Browne Date: June 16, 2004 Signature Barbara Browne
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SUPPLEMENTAL INFORMATION DISCLOSURE

Sheet 1 of 1

FORM PTO-1449 (Modified) ✓C1 JUN 21 2004 U.S. PATENT & TRADEMARK OFFICE	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO.: FIS920030379US1	SERIAL NO.: 10/708,055
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		APPLICANT: Lars Liebmann et al.	
(Use several sheets if necessary)		FILING DATE: February 5, 2004	GROUP: 1756

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	PATENT NUMBER	ISSUE DATE	PATENTEE	CLASS	SUB- CLASS	FILING DATE IF APPROPRIATE
AA						
AB						
AC						
AD						
AE						
AF						
AG						
AH						
AI						
AJ						
AK						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY OR PATENT OFFICE	CLASS	SUB- CLASS	TRANSLATION YES	NO
AL								
AM								
AN								
AO								
AP								

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

AQ	"Alternating Phase Shifted Mask for Logic Gate Levels, Design and Mask Manufacturing", Lars Liebmann, Ioana Graur, William Leipold, James Oberschmidt, David O'Grady, Denis Regaill; IBM Microelectronics, East Fishkill, New York and Burlington, Vermont. SPIE Conference on Optical Microlithography XII, March 1999, Vol. 3679, Pages 27-37.
AR	
AS	

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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